GENERAL IDP000230EN_01 (F80BVS_Breaker Position).xlsx

MODBUS TABLE ORGANIZATION

	Starting Address of the Group Registers (Dec)	Starting Address of the Group Registers (Hex)	System Version (Release)	System Version (Build)	Group Name (Text)	Group Code (Hex)	Group Complexity (Hex)	Group Version (Hex)	Object Code
ĺ	17408	4400	01	16	Breaker Position	54 02	10	01 00	

MODBUS PROTOCOL DETAILS

Function Code (Dec)	Exception Codes (Dec)	Data Encoding
2 (Read Discrete Inputs)	1, 2, 3	"Big Endian" (most significant byte first)
1 (Read Coils)	1, 2, 3	"Big Endian" (most significant byte first)
5/15 (Write Single/Multiple Coils)	1, 2, 3	"Big Endian" (most significant byte first)
4 (Read Input Registers)	1, 2, 3	"Big Endian" (most significant byte first)
3 (Read Holding register)	1, 2, 3	"Big Endian" (most significant byte first)
6/16 (Write Single/Multiple Holding register)	1, 2, 3	"Big Endian" (most significant byte first)

MODBUS OVER SERIAL DETAILS

Physical Layer	Trasmission Modes	Device Addressing	Baud Rates (bit/s)	Data Bits	Data bits trasmission sequence	Parity	Stop Bits
standard EIA/TIA 485 (RS-485) two-wire configuration	RTU	1÷247	programmable (1200, 2400, 4800, 9600, 19200, 38400)	8	Least significant bit first	NONE	1

MASTER/SLAVE COMMUNICATION TIMING

Timer Description	Timer Value (msec)
Inter-character time-out	< 1,5 character times
Response delay (from master request)	-
Delay Time (between two master trasmissions)	-

- MODBUS over serial line specification and implementation guide V1.02 - MODBUS APPLICATION PROTOCOL SPECIFICATION V1.1b REFER ALSO TO: www.modbus.org

NOTE: File and printed copies of this document are not subject to document change control. IDP000230EN_01 (F80BVS_Breaker Position).xlsx DISCRETE INPUT - Bits (R)

Register Number	Register Address (Dec)	Register Address (Hex)	Dimension [bit]	Description	Note	Read Function Codes (Dec)	Data Storing
17409	17408	4400	3	Breaker Position			
17409	17408	4400	1	Drawn-out	See Note 1	2	
17410	17409	4401	1	Inserted	See Note 1	2	
17411	17410	4402	1	Test	See Note 1	2	

Note 1
The information reported here "self-resets" when the condition that generated it ends.

REV: 1.04

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Register Number	Register Address (Dec)	Register Address (Hex)	Dimension [bit]	Description	Note	Read Function Codes (Dec)	Write Function Codes (Dec)	Data Storing		
(no COILS availables)										

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Register Number	Register Address (Dec)	Register Address (Hex)	Dimension [word]	Bit Position	Description	Туре	Scale	Unit	Range	Note	Read Function Code (Dec)	Data Storing
(no INPUT REGISTERS availables)												

IDP000230EN_01 (F80BVS_Breaker Position).xlsx

Register Number	Register Address (Dec)	Register Address (Hex)	Dimension [word]	Bit Position	Description	Туре	Scale	Unit	Range	Note	Read Function Codes (Dec)		Data Storing
(no HOLDING REGISTERS availables)													